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The Elimination of Whiskers from Electroplated Tin

by
Masanobu Tsujimoto, Shigeo Hashimoto, Masayuki Kiso, Raihei Ikumoto, Toshikazu Kano and Genki Kanamori
C. Uyemura & Co., Ltd.
Hirakata, Japan
and
Don Gudczauskas and George Milad*
C. Uyemura & Co., Ltd.
Southington, Connecticut, USA

ABSTRACT

As RoHS lead-free regulations began to take hold globally, tin and its alloys were the first choice as an alternative to eutectic tin/lead. On the solder side, the transition has moved forward and solutions have been implemented, such as the Sn-Ag-Cu (SAC) family of lead-free (LF) solders, for paste reflow and tin/copper for hot air solder leveling (HASL). The industry is constantly making progress adapting its materials and processes to the higher reflow temperature profile for these LF solders. Today there is a much better understanding of the nature of the intermetallic (IMC) bond as well as the reliability of LF solder joint.

On the surface finish side, replacing tin/lead has posed greater challenges. Component leads and connector finishes were being converted to tin as an obvious alternative. This works well as a soldering surface. However, any part of the lead or the connection surface that is not soldered to has shown a tendency to form tin whiskers over the life of the part. Internal stresses in the deposit due to IMC formation or external stresses on the deposit are known to initiate whisker formation.

In this paper two approaches are implemented to dissipate the stress that is formed. The first is to modify the substrate surface to control the growth in thickness and direction of propagation of the IMC. The second is to modify the large columnar tin deposit crystal structure to mimic the fine equiaxed structure of tin/lead solder. The former is achieved thru controlled micro-roughening of the substrate and the latter by the use of additives to the plating bath. Data will be presented to show that by implementing these two modifications, the stress causing tin whiskers is dissipated and tin whisker formation is inhibited.

Keywords: tin whisker inhibition, tin/lead solder replacement

Introduction

Electroplated pure tin and tin-based alloys are being used as alternatives to tin/lead in the majority of electronic components. These alternatives are known to produce tin whiskers resulting in short circuits on these components.

In the case of a tin finish on copper and copper-based alloys, the major cause of tin whisker formation is compressive stress. The stress is mainly caused by irregular growth of a copper-tin intermetallic compound (IMC) at ambient conditions.

*Corresponding author:

Mr. George Milad
National Accounts Manager for Technology
Uyemura International Corporation
240 Town Line Rd
Southington, CT 06489
Phone: (860) 793 4011
Mobile: (516) 901 3874
E-mail: GMilad@uyemura.com

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It is known that tin whiskers are readily formed on electroplated tin deposits on copper and are not observed on electroplated tin/lead deposits. The crystal structures of tin and tin-lead deposits are different. The crystal structure has a direct impact on tin whisker formation.

A tin deposit with a modified crystal structure (similar to tin-lead deposits) is capable of preventing whisker formation by dissipating and delocalizing the stress that cause whiskers.

As shown in Fig. 1, stress, channeled along the boundaries of the large grained columnar tin deposit, is responsible for the emergence of tin whiskers. Stress may be internal or external (Fig. 2). The primary source of internal stress is attributed to the non-uniform increase in the thickness of the IMC layer over time at ambient conditions (30°C, 60%RH for 4000 hr). Another condition that produces internal stress is exposure to high temperature and high humidity (55°C, 85%RH for 4000 hr) for extended periods of time, which gives rise to oxidation and/or corrosion. Internal stress could also be induced by thermal cycling (-55°C to 85°C, 1500 cycles) due to mismatched coefficients of thermal expansion. The latter two forms are commonly used to induce internal stress in controlled experiments. External stress is also known to initiate whisker growth. An example is the stress induced by press fit connectors.

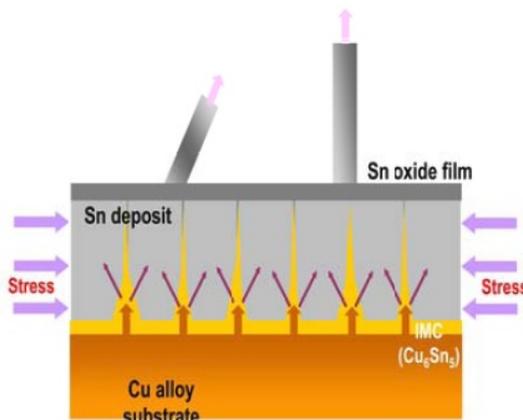


Figure 1 - Schematic diagram of tin whisker formation.

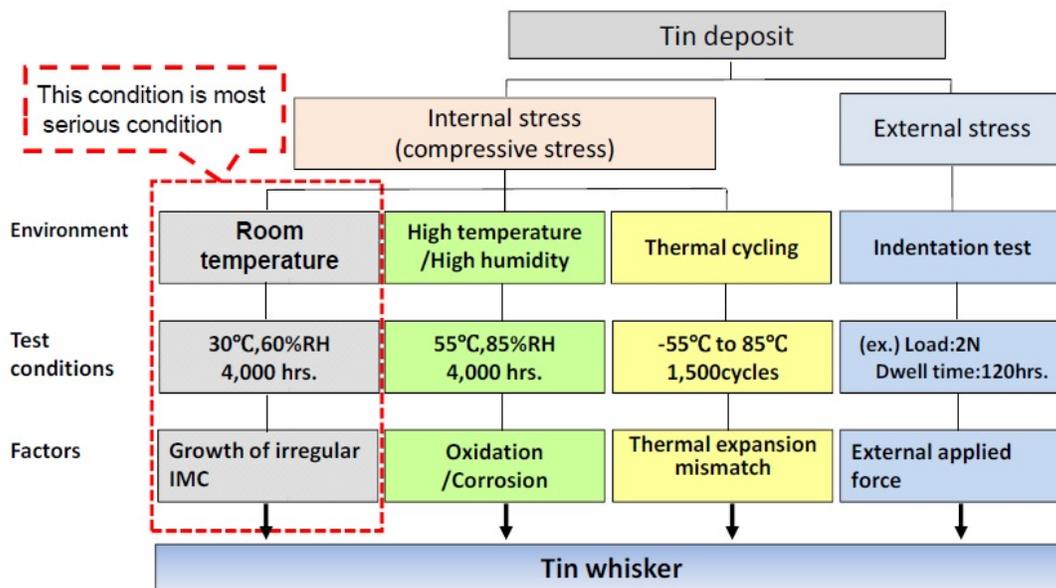


Figure 2 - Four different paths leading to stress and whisker formation.

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The approach taken in this study is to control the thickness of the IMC, as well as modifying the crystal structure of the tin deposit from a large-grained columnar to a small-grained equiaxed structure. The former is achieved by increasing the area of the copper substrate, through chemical micro-roughening. The grain structure is altered by the use of specific chemical additives to the plating bath. All testing was done under ambient conditions noted above.

Copper surface modification

A study was conducted on the morphology of the copper substrate prior to plating. The test vehicle was a CDA-19400 lead frame (Cu-2.3 Fe-0.03 P-0.12 Zn) (Fig. 3). A series of substrates varying in roughness were evaluated for whisker formation after electroplated tin deposition. The roughness was controlled by chemical etching procedures. Average roughness R_a , varied between 0.13 to 0.47 μm . As shown in Fig. 4, an R_a value of 0.47 μm has a much larger surface area as compared to an R_a value of 0.13 μm . The propensity to whisker was evaluated as follows:

Tin plating

The plating bath was methane sulfonic acid (MSA)-based matte tin. The plating was run at a current density of 10A/dm². The plating time was varied to produce a 3- μm and a 10- μm thick deposit. The former was for short term whisker evaluation and the latter, which is typical of lead frame plating, was used for long term evaluations.

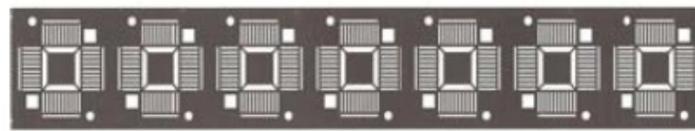


Figure 3 - Test vehicle.

Methodology

The test vehicles were subjected to chemical micro-roughening to produce a set of specific R_a values (Fig. 4). The figure shows the SEM micrographs of the different degrees of micro-roughening (R_a), as measured in μm .

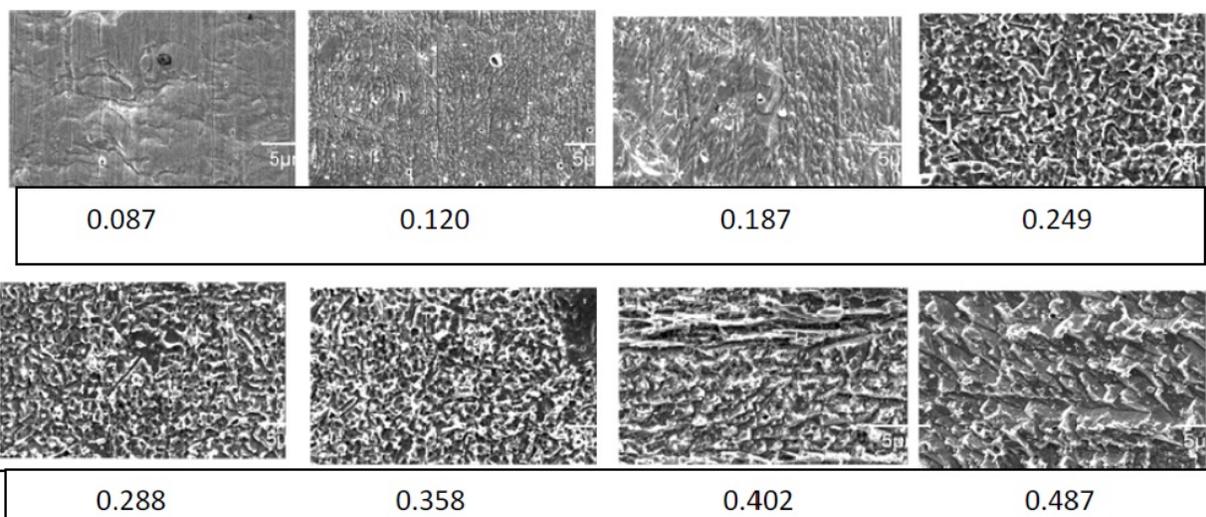


Figure 4 - SEM micrographs of different R_a values.

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The samples were then run thru a standard plating process as outlined in Fig. 5. The samples were then stored under controlled ambient conditions (30°C, 60%RH) for extended periods of time (1000 hr). The samples were examined for whisker formation at various time intervals.

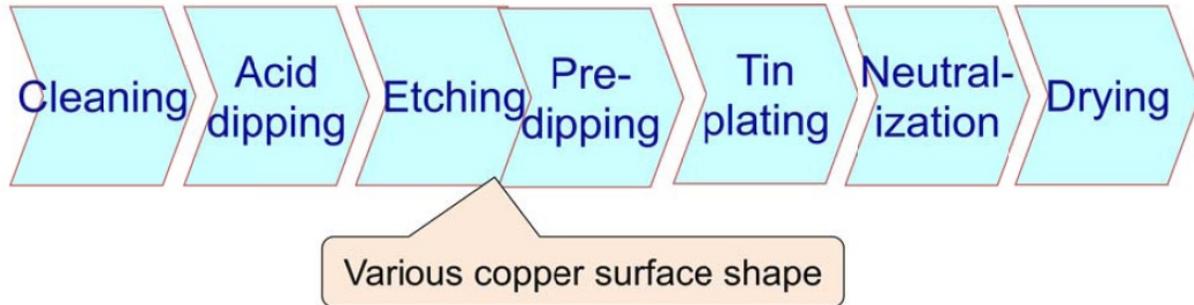


Figure 5 - The plating process sequence used for this study.

Definition of a "Whisker"

A whisker is a protrusion that is >10 µm in length and that has an aspect ratio >2.

Measurement of whisker length

The measurement according to JEITA ET-7410 is the straight line distance from the point of emergence of the whisker to the most distant point on the whisker.

Results and discussion

The whiskers were examined, measured and tabulated after 1000 hr of storage under controlled ambient conditions (30°C, 60%RH). The data gathered from whisker examination on the various morphologies of roughening are plotted in Figs. 6 and 7. Figure 6 a plot of maximum whisker length as a function of roughness. Figure 7 shows the whisker density per mm² as a function of roughness. The data clearly indicates that there is clear correlation between surface roughness and whisker propensity. The rougher surface produces lower whisker length and also lower density per mm².

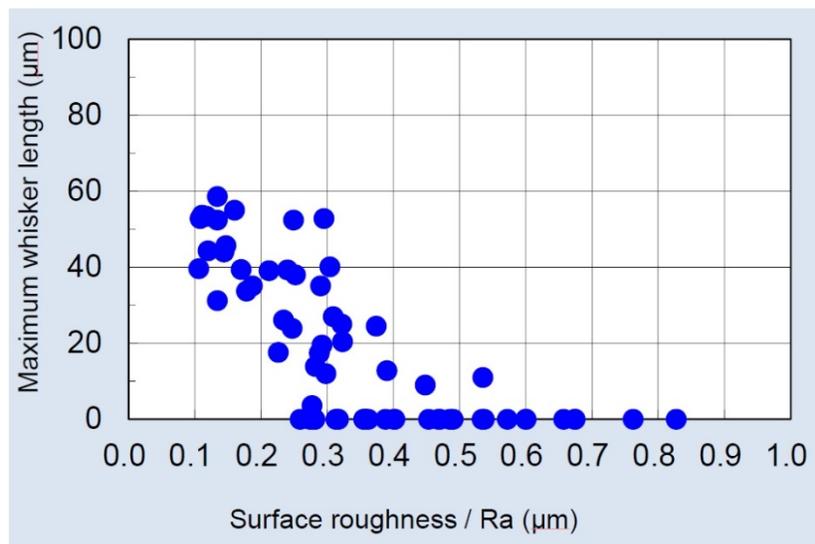


Figure 6 - Maximum whisker length vs. surface roughness.

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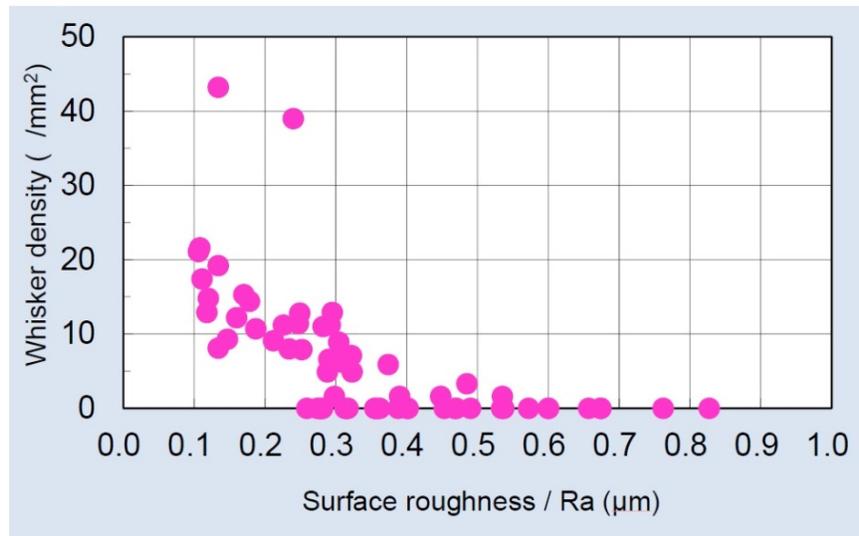
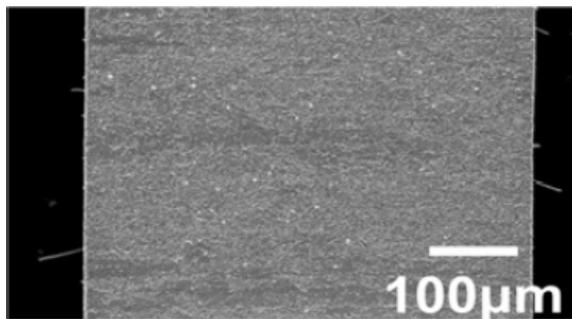
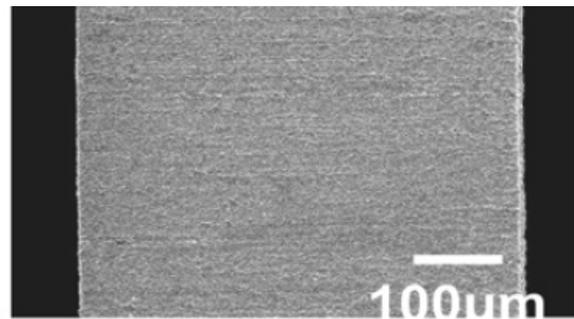


Figure 7 - Whisker density vs. surface roughness.

Figure 8 shows whisker growth on 3 µm of tin plated on smoother copper ($R_a = 0.13 \mu\text{m}$) as compared to no whiskers on the rougher surface ($R_a = 0.47 \mu\text{m}$)



$R_a = 0.13 \mu\text{m}$, whiskers



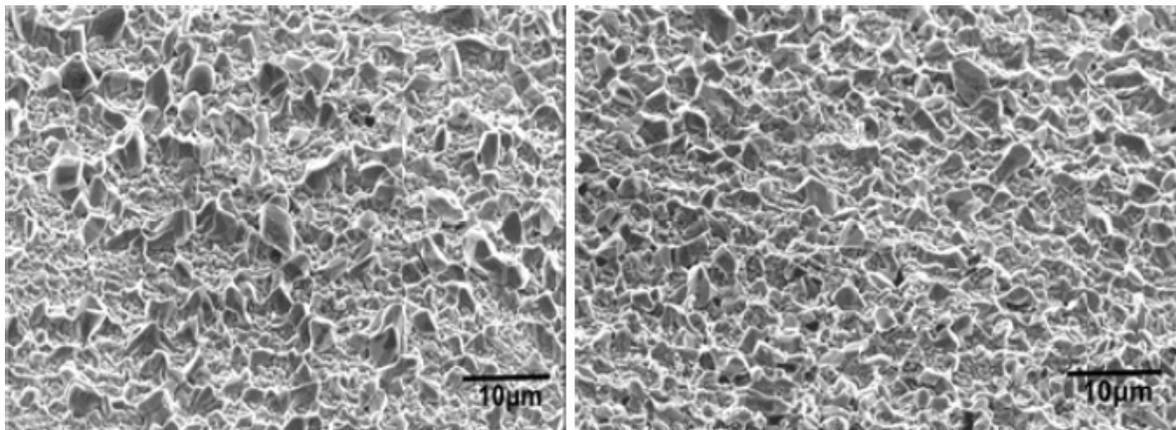
$R_a = 0.47 \mu\text{m}$, no whiskers

Figure 8 - 3-µm tin deposit after 1000 hr at 30°C, 60%RH.

In an effort to explain this, more work was done. The reflowed/soldered layer on 10 µm of tin samples, that were stored for 7000 hr at 30°C, 60%RH, was stripped by chemical means and the intermetallic (IMC) morphology was examined. In addition, cross sections were prepared and examined to verify the top down observation.

Figure 9 shows the top view of the IMC after tin stripping for the two extremes of R_a , namely $R_a = 0.13 \mu\text{m}$ and $R_a = 0.47 \mu\text{m}$. Figure 10 shows cross-sections of the same R_a values. It is clear that the rougher R_a of 0.47 µm produced a thinner, more uniform IMC, compared to the smoother R_a of 0.13 µm, which showed increased IMC thickness in localized areas. A plausible explanation is that the IMC is spread over a much larger area on the rougher morphology ($R_a = 0.47 \mu\text{m}$) as compared to the smaller area of the smoother surface ($R_a = 0.13 \mu\text{m}$). It follows then that the stress resulting from IMC formation would be highly reduced and dissipated with increased surface roughness of the underlying copper substrate.

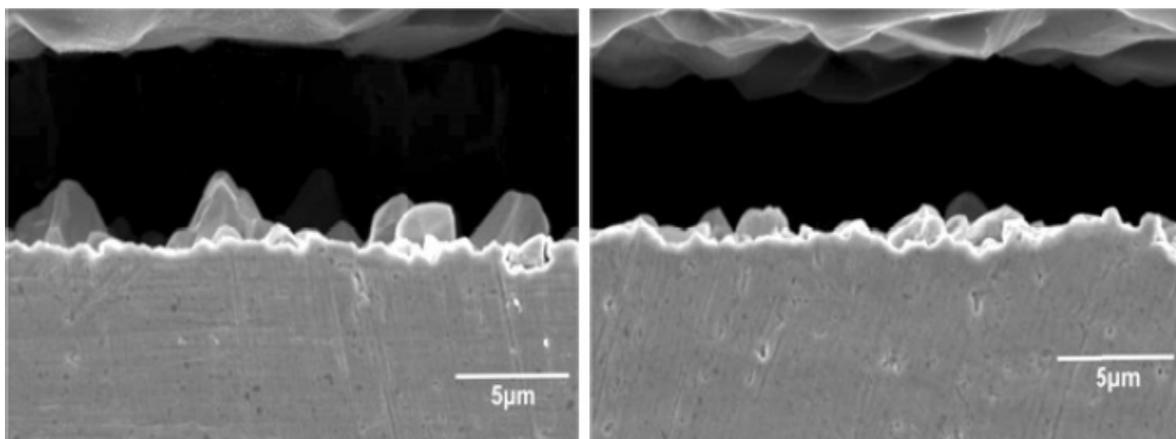
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$R_a = 0.13 \mu\text{m}$

$R_a = 0.47 \mu\text{m}$

Figure 9 - Morphology of the IMC surface after tin stripping.



$R_a = 0.13 \mu\text{m}$

$R_a = 0.47 \mu\text{m}$

Figure 10 - Cross section showing the IMC after tin stripping.

The solderability and the ductility of a 10- μm tin deposit on the two extremes of surface roughness were examined using "Wetting Balance Testing" as well as the "Bend Test." There was virtually no difference in performance as seen in Fig. 11.

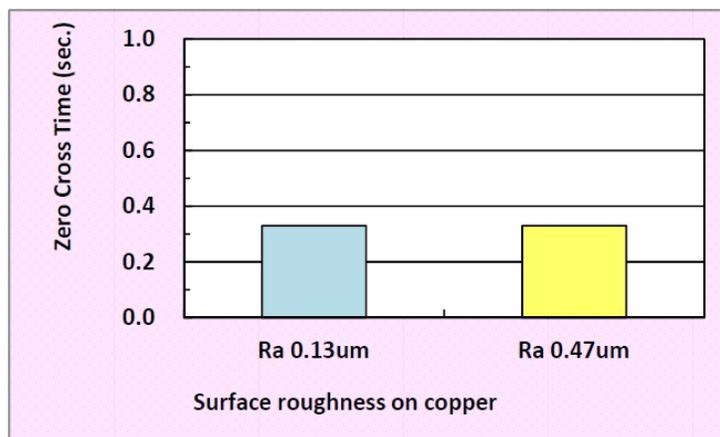


Figure 11 - Comparison of zero cross time of 10- μm tin deposits for two levels of roughness.

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Modifying the crystal structure of the tin deposit

A close examination of the crystal structure of both tin and Sn/Pb alloy shows a clear difference between the two deposits. The Sn/Pb which does not whisker has an equiaxed, relatively fine-grained deposit. The tin on the other hand shows larger columnar crystals. Figure 12 shows the difference in crystal structure between tin and eutectic Sn/Pb alloy.

It is believed that, if the crystal structure of the tin deposit can be modified to the Sn/Pb crystal structure, the stresses will be dissipated and whiskers will not form. Tests were conducted using the same test vehicle and the same plating conditions as outlined earlier in the copper surface roughness study.

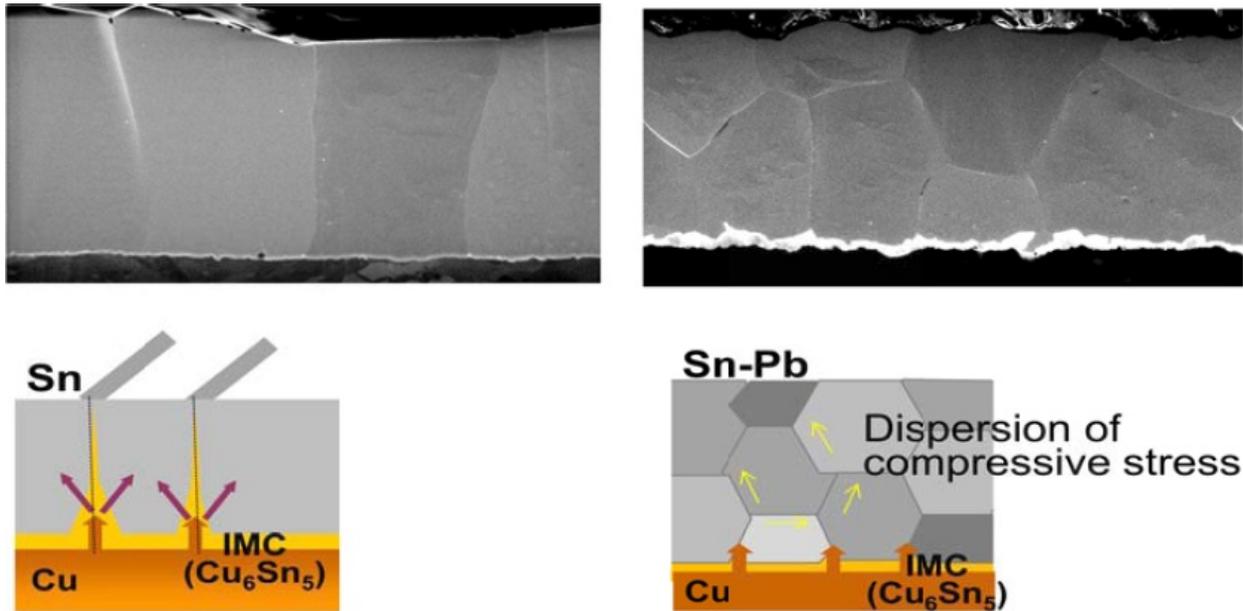
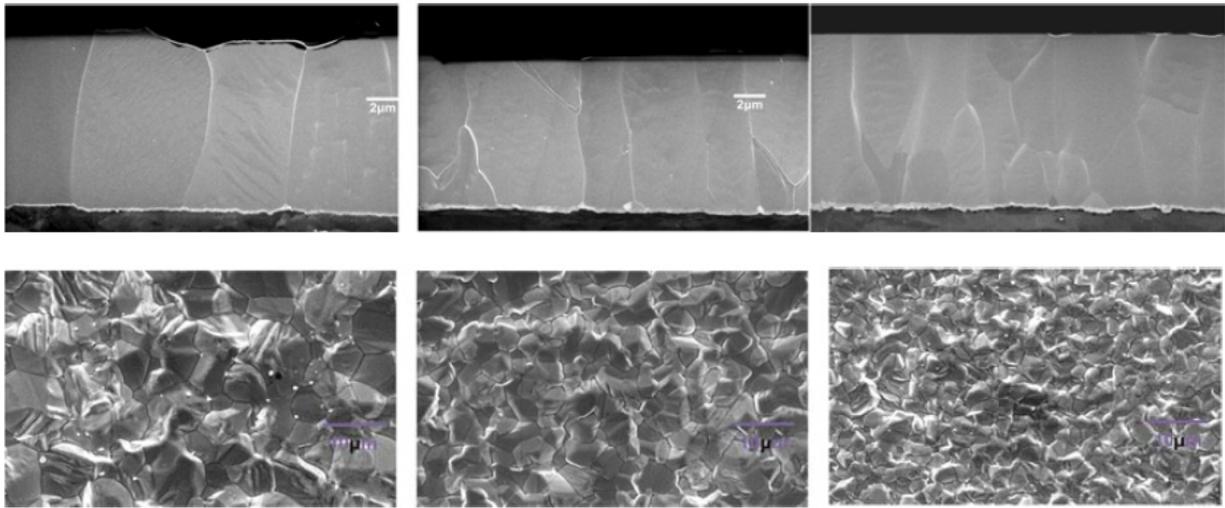


Figure 12 - SEM and schematics of the tin vs. the Sn/Pb deposit structures.

Three types of tin deposits were produced by the use of specific plating additives to the bath. Type A is a standard tin deposit characterized by large columnar crystals, type B was modified to produce smaller columnar grain structure and type C was further modified to produce a still smaller grain that is both columnar as well as equiaxed, almost mimicking the Sn/Pb structure. The SEM results are shown in Fig. 13.

All three types were plated to the typical thickness of 10 μm (the thickness typical of lead frames) and were placed in an ambient environment (30°C, 60%RH) for 4000 hrs. Figure 14(a) shows that relatively long whiskers developed on the Type A tin deposit. Figure 14(b) shows whiskers that were shorter than those of type A developed. Figure 14(c) shows no whisker formation with the type C crystal structure stored under the same conditions. Figure 15 shows a graph depicting the length of whisker versus storage time for the three tin deposit crystal types.

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Type A

Type B

Type C

Figure 13 - SEM photos of the cross-section and surface morphology in the three types of tin deposits.

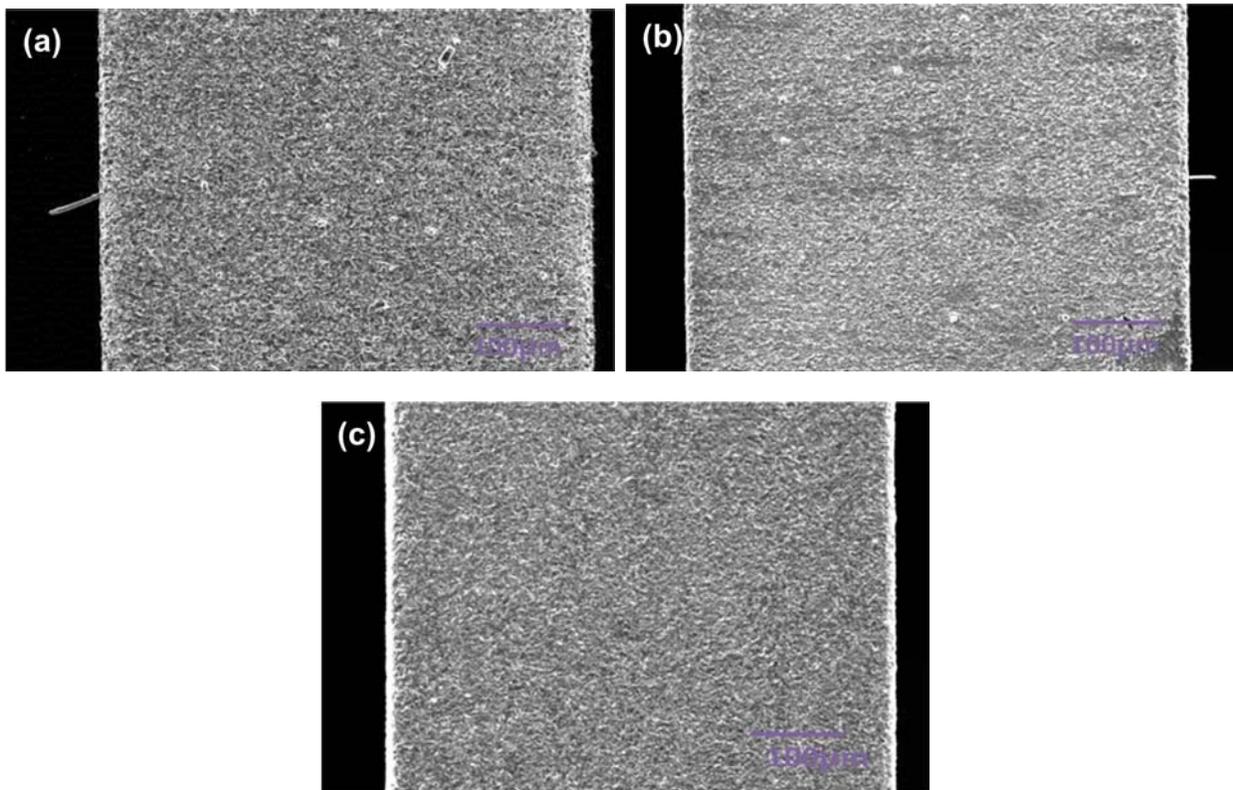


Figure 14 - The surface morphology of the three types of tin deposits after 4000 hr of exposure in an ambient environment (30°C, 60%RH): (a) Type A, whiskers (long); (b) Type B, whiskers (short); (c) Type C, no whiskers.

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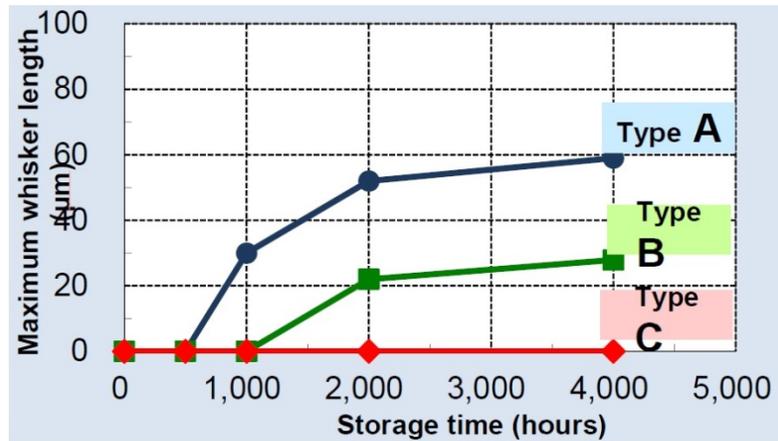


Figure 15 - Storage time vs. maximum whisker length for the three types of tin structures.

Combining the results to produce a whisker-free system

Figure 16 is the result of combining the rough copper structure ($R_a = 0.45 \mu\text{m}$) with the fine-grained equiaxed crystal structure achieved by modifying the plating bath with specific types of additives. The result is a very controlled, evenly distributed and relatively thin IMC producing minimum stress. The equiaxed crystal structure dissipates the reduced stress, resulting in no whisker formation. This combination produced no whiskers after 22,000 hr.

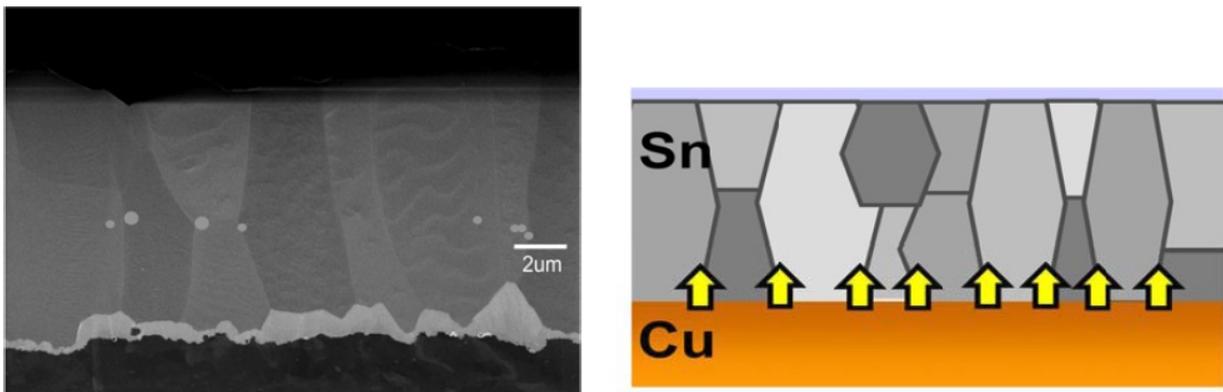


Figure 16 - SEM of a cross-section of the combination of the rough copper structure with the fine-grained equiaxed crystal structure and a graphic presentation of same.

Conclusions

- Tin deposits on rough copper (higher R_a values) reduced whisker formation under ambient conditions. Rougher copper forms a uniform IMC layer, and prevents the localization of internal stress.
- Compared with large grain size tin deposits, tin deposits having a small grain size reduced tin whisker formation under ambient conditions.
- Tin deposits having a crystal structure similar to that of a tin-lead deposit effectively restrained tin whisker formation.
- The crystal structure in tin deposits is one of the most important factors in restraining tin whiskers.



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About the author



George Milad has 30 years of experience in PWB manufacturing. He is the National Accounts Manager for Technology at Uyemura International Corporation. Other positions held include: Technical Marketing Manager at RHEM, Director of Applications at Atotech and Engineering Manager at Automata PWB. He is the author of the chapters on "Plating" and "Surface Finishing" in Clyde Coomb's Printed Circuit Handbook, Fifth Edition, 2001. He is the recipient of the IPC 2009 President's award. He presently chairs the Plating Committee and is a permanent member of the Technical Activities Executive Committee of the IPC.